

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:	Balboni et al.	GROUP:	2817
SERIAL NO:	10/679,023	EXAMINER:	Joseph Chang
FILED:	10/03/03	Date of mailing of Notice of Allowance:	11/18/07
FOR:	PHASE-LOCKED LOOP BANDWIDTH CALIBRATION CIRCUIT AND METHOD THEREOF		

Mail Stop Issue Fee
Assistant Commissioner of Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Attn: Official Draftsman

TRANSMITTAL OF FORMAL DRAWINGS

Applicant submits herewith new drawing(s) for this application. Attached please find NINE (9) sheets of formal drawings for this application.

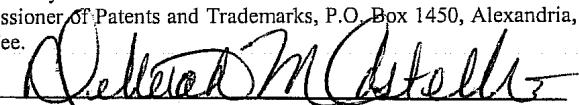
The three month period of response set in the Notice of Allowability (PTOL 37) expires on February 8, 2008, and this submission is on or before this expiry date.

Respectfully submitted,


Matthew E. Connors

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited electronically with the United States Postal Service on the date shown below addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.


Deborah M. Costello

Date: 2-5-08